

CLOCK BASED VOLTAGE DEVIATION DETECTOR

ABSTRACT OF THE DISCLOSURE

The clock based voltage deviation detector of the present invention includes a pulse
5 module, an indicator module and a correlation module. The pulse module generates a stream
of reset pulses as a function of a clock signal. The indicator module generates a pass/fail
indicator signal as a function of the reset pulse stream and a difference between an input
signal and a reference voltage. The correlation module correlates an event (e.g., overvoltage
or undervoltage) of the pass/fail indicator signal with a period of the clock signal at which the
10 event occurred.